

ABSTRACT

A current driving apparatus includes a first square wave generator (100), a second square wave generator (200), an FET (Field Effect Transistor) (3), and a power supply (9). The first square wave generator has an output connected to the second square wave generator's input. The second square wave generator has an output connected to the FET gate. A current clamping resistor (11) is provided between the FET source and the power supply. The FET drain provides current to a possible load (not shown). The first square wave generator generates a low frequency square wave signal for timing control, and the second square wave generator generates a high frequency square wave signal for amplitude control.